Remarks

Applicant thanks the examiner for the examination of the prior application.

New claims 25-44 better define the claimed inventions.

The present claims distinguish over the cited art by requiring the protocol selection memory to be coupled to the serial scan path or the serial data input lead and requiring the event circuit to be coupled to the protocol selection memory.

Laid-open application EP 0,315,475 and the 1988 paper by Whetsel, "A Proposed Standard Test Bus and Boundary Scan Architecture" appear to disclose some limitations of the present claims.

Comments concerning this reference appear in Attachment A to IDS-B. The EP publication appears not to be prior art to this application.

The claim limitation of the "protocol selection memory" finds support in the present application through incorporation by reference of US 5,001,713. That patent issued from Application No. 07/308,272, filed February 8, 1989. The incorporation by reference of this patent occurred in the present application as filed. See the original application, page 11, end of the first paragraph.

The present application provides a disclosure of the Event Qualification Module, EQM, 32 with a description of the matter in Figure 7. The present application concludes the description of Figure 7 by saying: "The operation and protocol of the Event

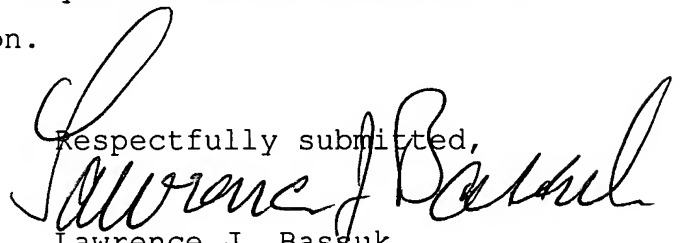Qualification Module are described in" the cited US 5,001,713 and US 5,103,450.

In Figures 6 and 7 of US 5,001,713, the drawing depicts control register 88, which corresponds to the claimed "protocol selection memory", and leads C0 and C1, which carry the protocol selection signals to the EQM. At Column 9, Line 45, that patent says: "The SDI signal is input to a control register 88. The control register 88 outputs signals C0, C1 and I/E to an EQM controller 90."

At Column 11, Line 47, that patent says: "The C0 and C1 inputs come from two scannable bits in the EQM control register 88. C0 and C1 provide the required two-bit command input for the EQM controller to perform one of four event qualification operations described below."

The next paragraph in US 5,001,713 describes the enable signal of present claim 35.

The specification is in allowable form and the claims distinguish over the art. Applicant requests reconsideration or further examination of this application.

Respectfully submitted,

Lawrence J. Bassuk
Reg. No. 29,043
Attorney for Applicant

Texas Instruments Incorporated
P. O. Box 655474, MS 3999
Dallas, Texas 75265
972-917-5458